

EE412 Black Magic (BM) Pro 4” Graphene Development and Optimization

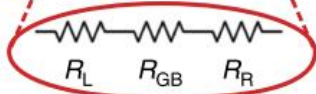
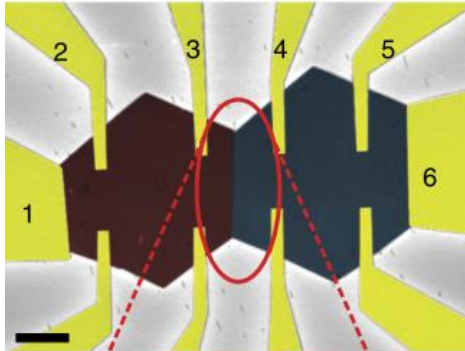
6-3-2015

Users: N. Wang, C. Neumann

Staff Advisor: Dr. Yong Cheol Shin

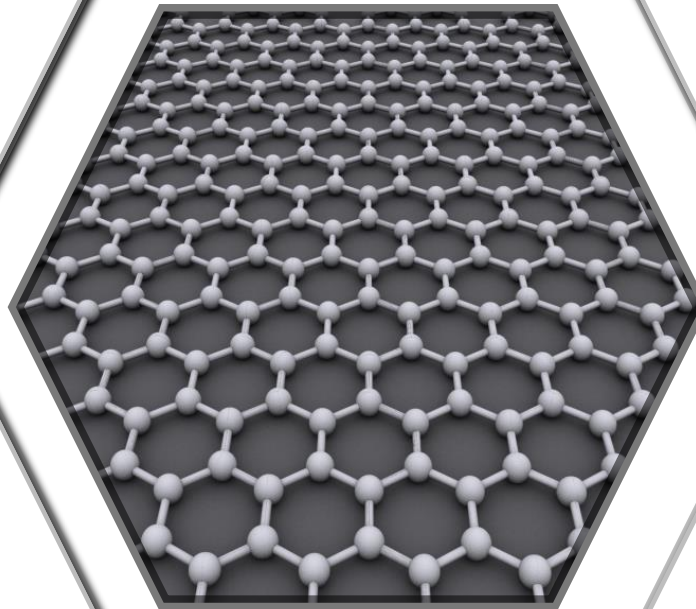
SNF Staff Advisors: Michelle Rincon, Ted Berg,
Robert Chen

Next-Gen Sensors



P. Yasaei, et al., *Nat. Comm.*,
(2014)

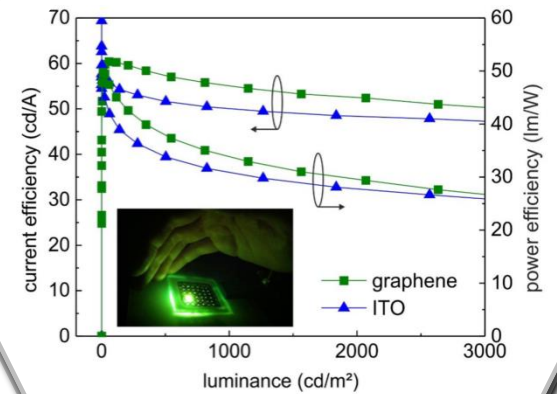
“Beyond Moore” Electronics



w/ Graphene

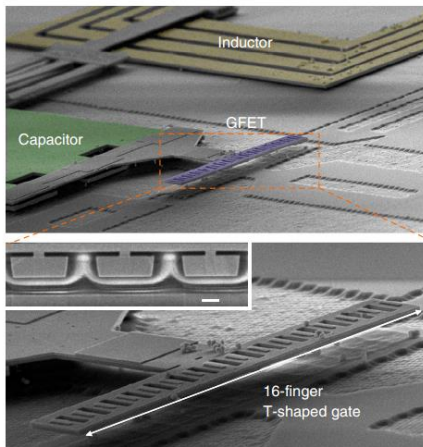
High electrical mobility
Thin & Flexible

Transparent Electronics



J. Meyer, et al., *Sci. Rep.*, **4**,
5380 (2014)

Analog Electronics



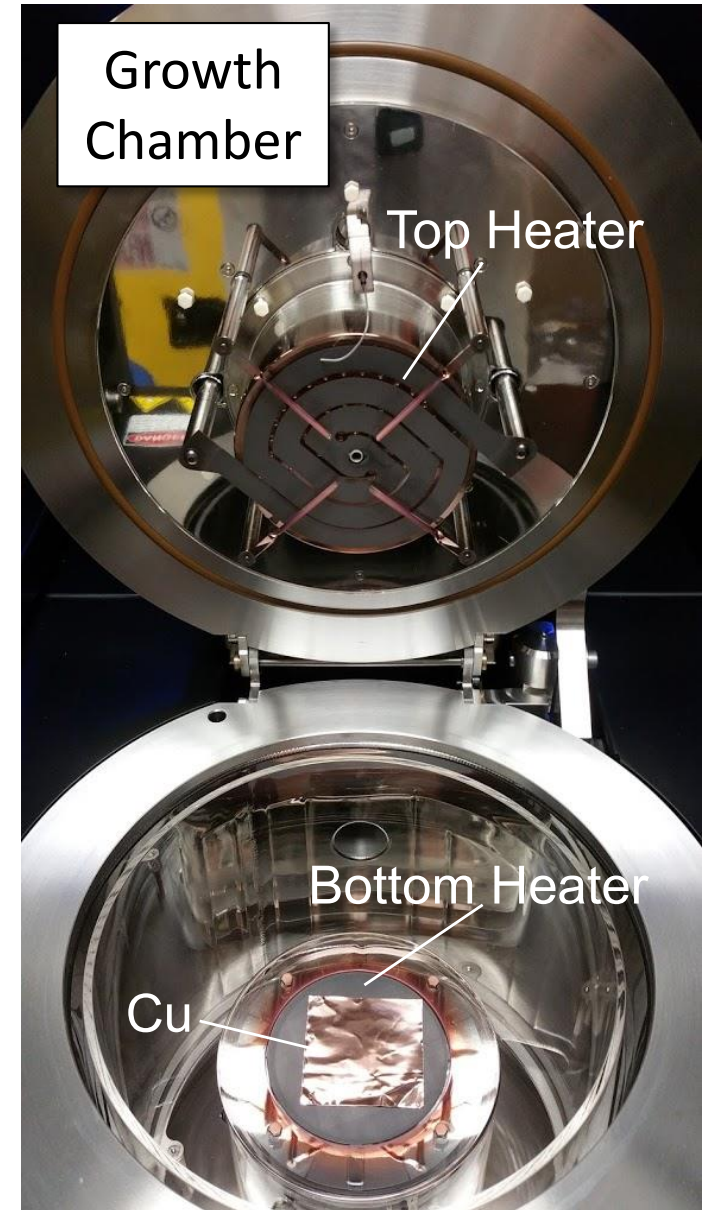
S.-J. Han, et al., *Nat. Comm.*,
5, 3086 (2014)

Flexible Electronics

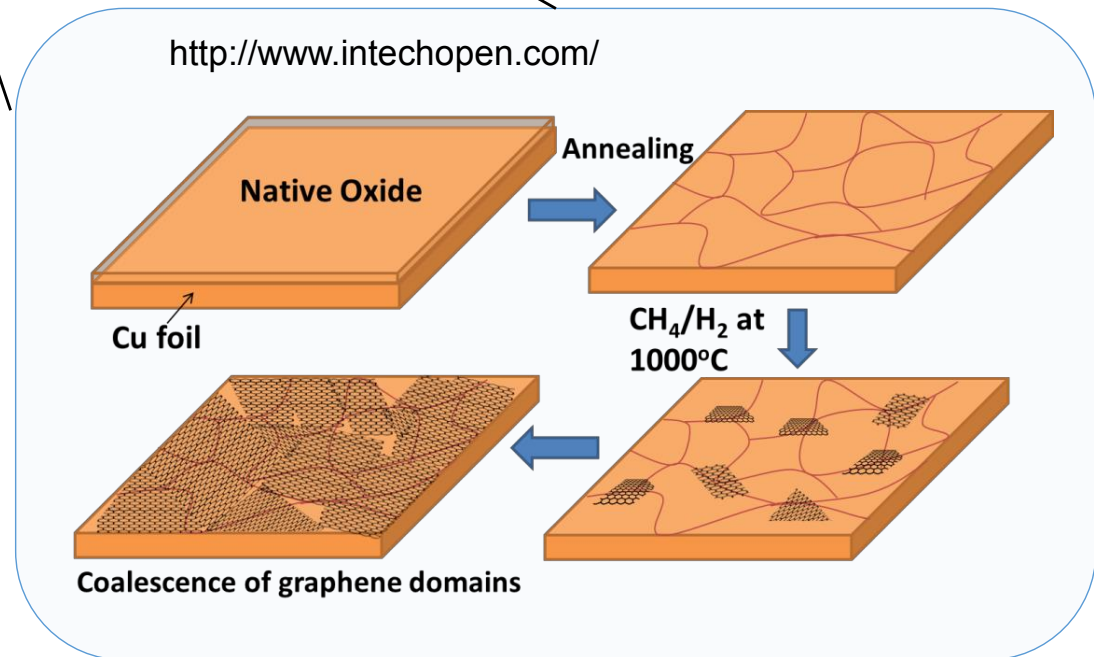
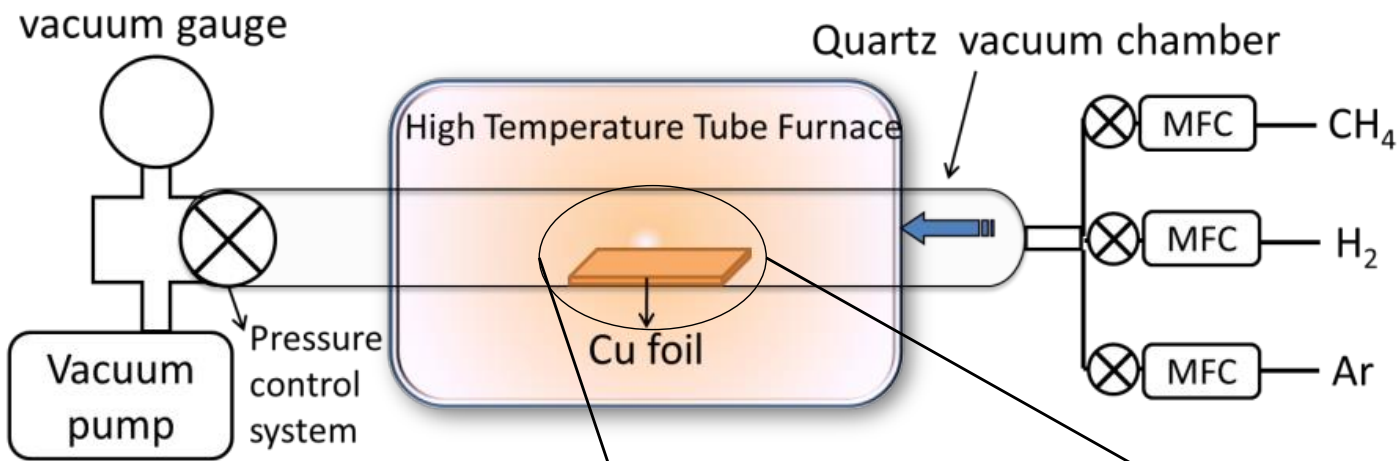


Plastic Logic

Graphene @ SNF



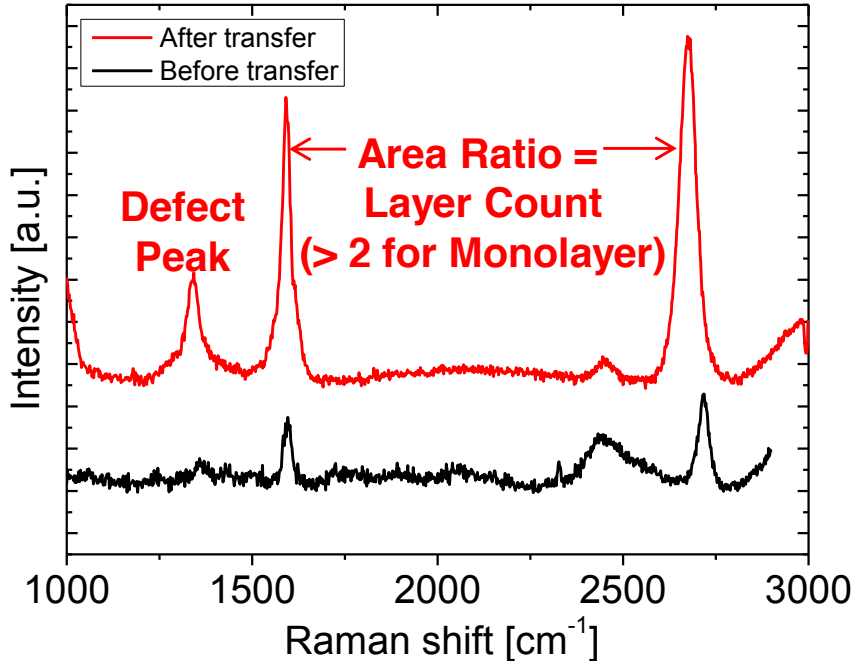
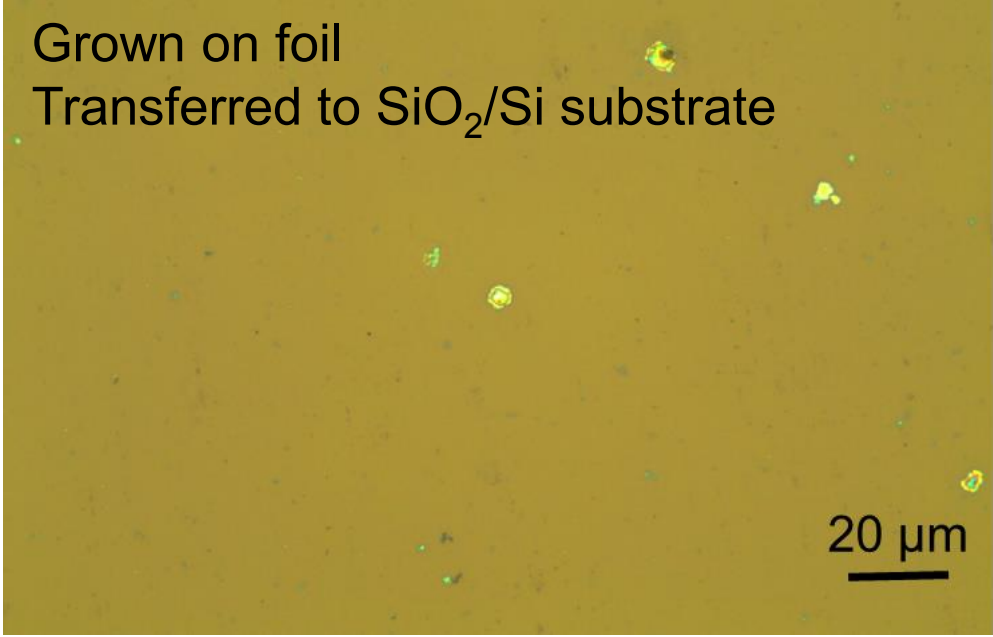
Chemical Vapor Deposition of Graphene



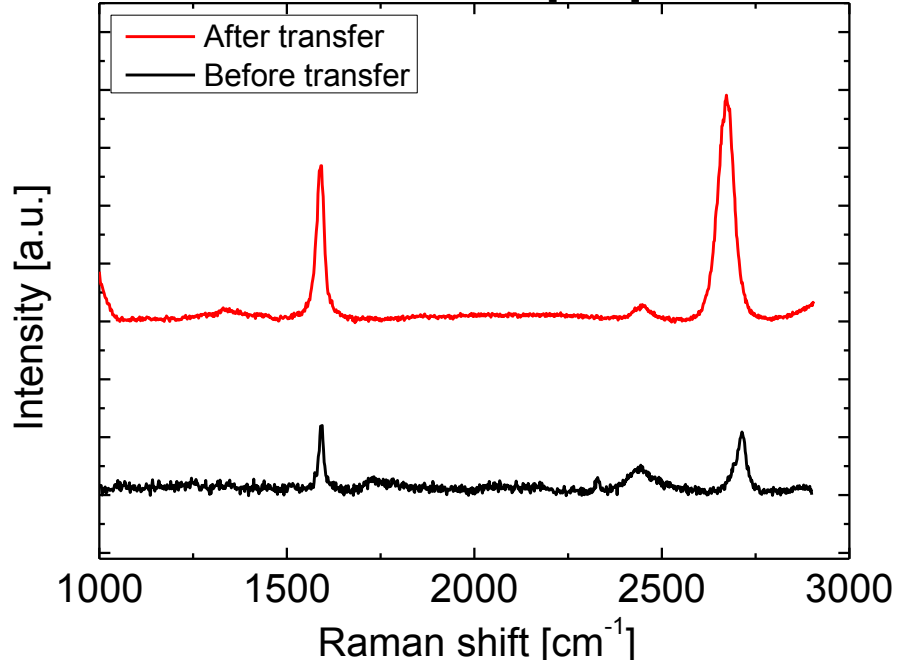
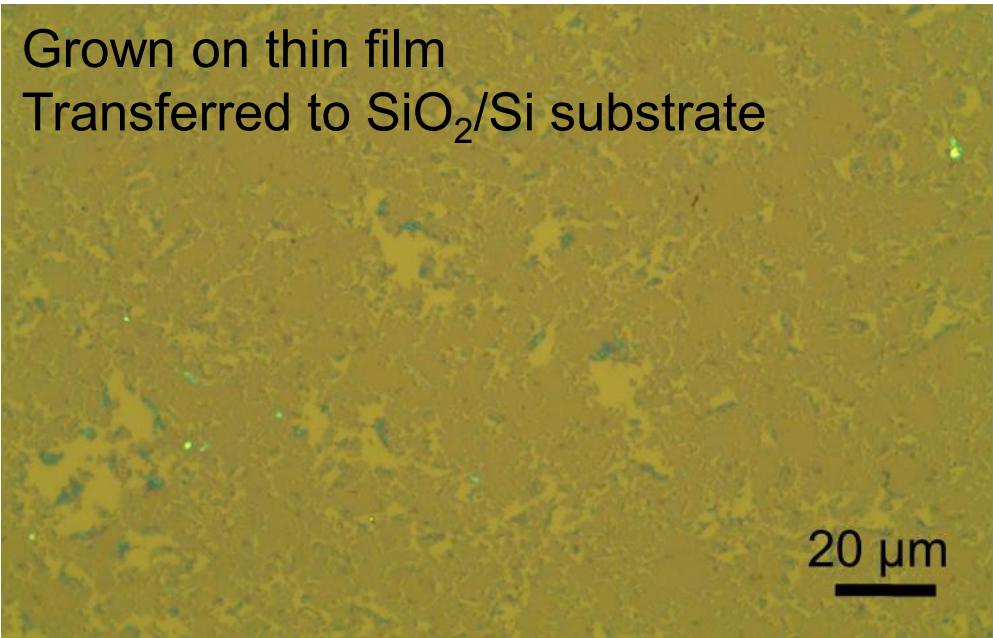
Growth Development

Raman Results - Aixtron Base Recipes

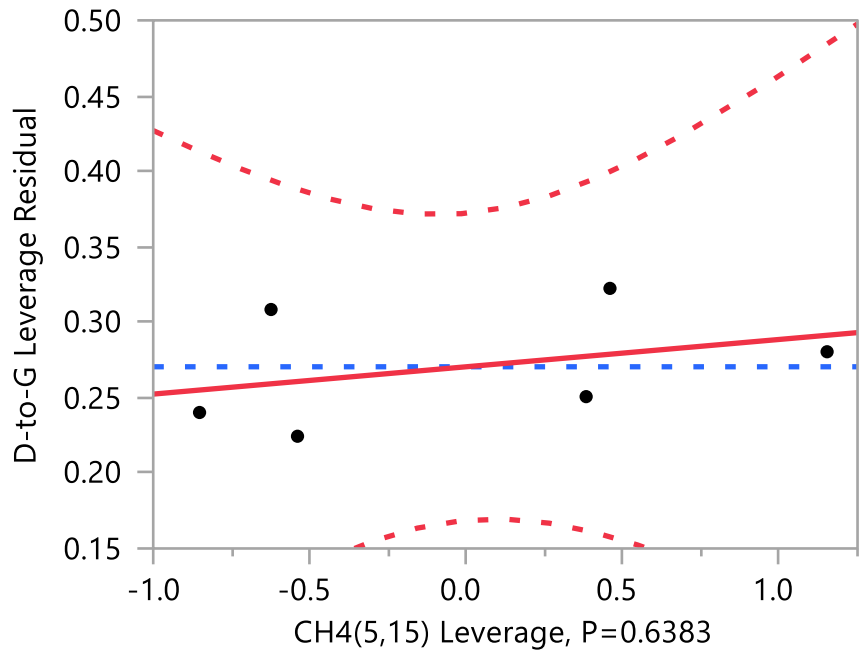
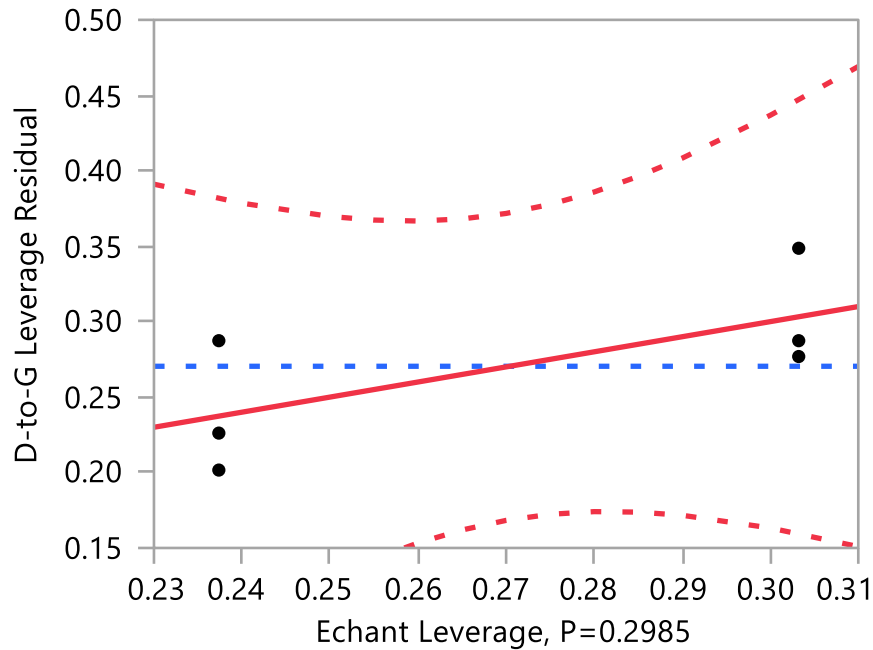
Grown on foil
Transferred to SiO₂/Si substrate



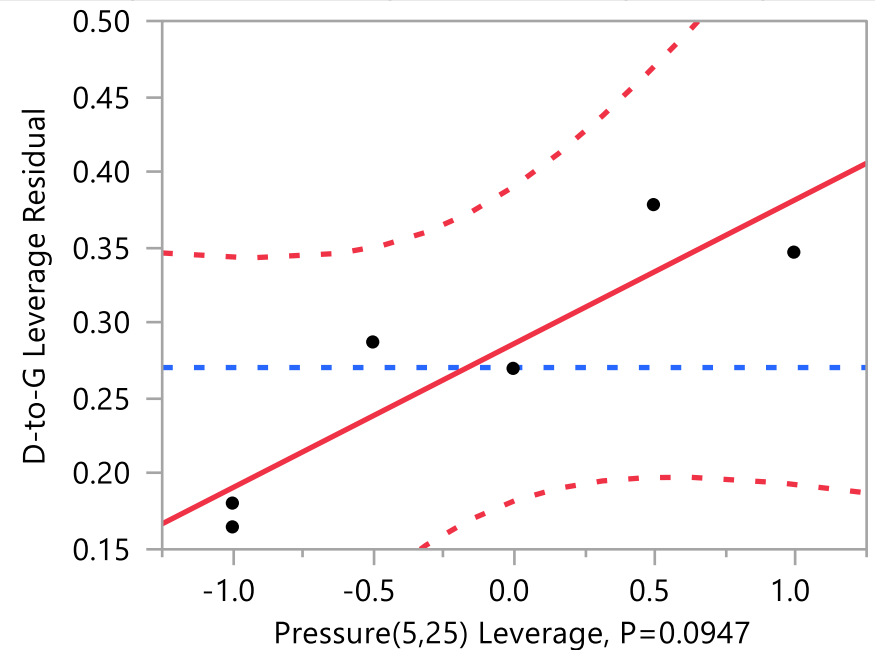
Grown on thin film
Transferred to SiO₂/Si substrate



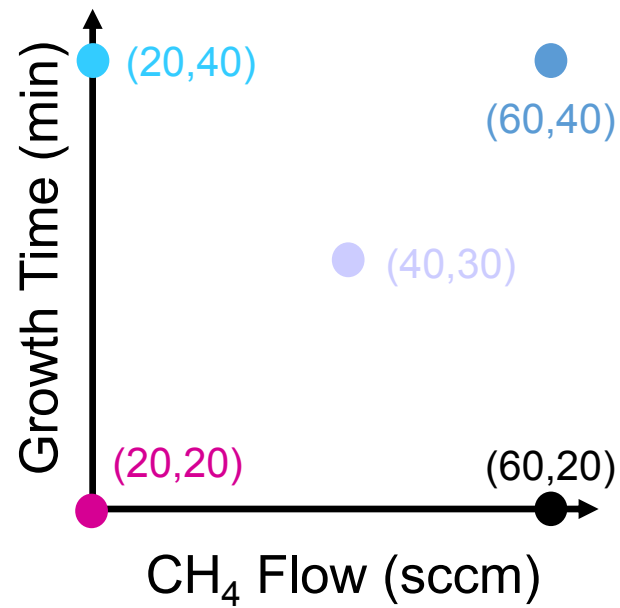
Foil Growth



Parameters			Results	
Etch	CH ₄ (sccm)	Pres (mb)	D/G	2D/G
Ni	10	5	0.19	3.5
Ni	15	25	0.48	2.64
Ni	5	10	0.24	2.51
HCl	5	5	0.18	3.01
HCl	10	25	0.48	2.64
HCl	15	10	0.21	3.32

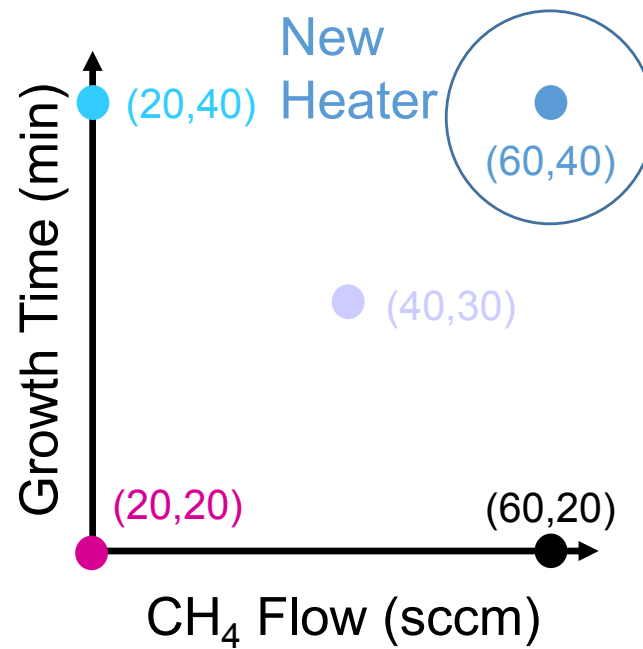


Thin Film Growths



Run	Center		Edge	
	D/G	2D/G	D/G	2D/G
(20,20)				
(60,20)				
(20,40)				
(40,30)				
(60,40)				

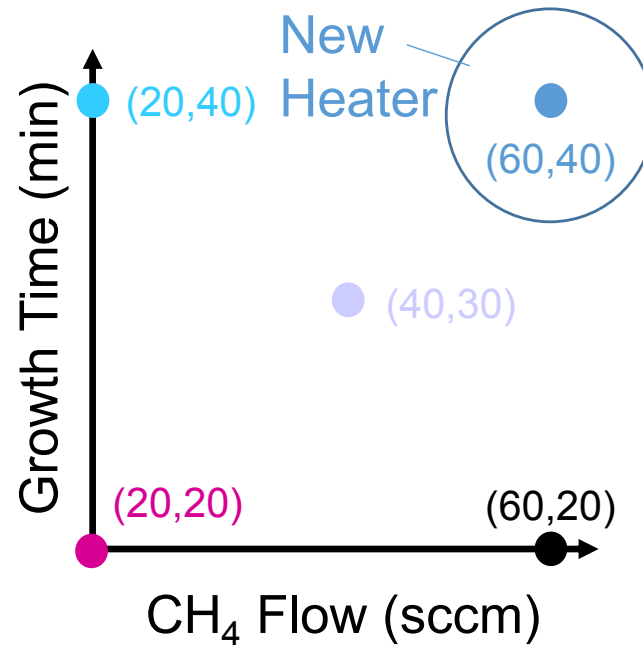
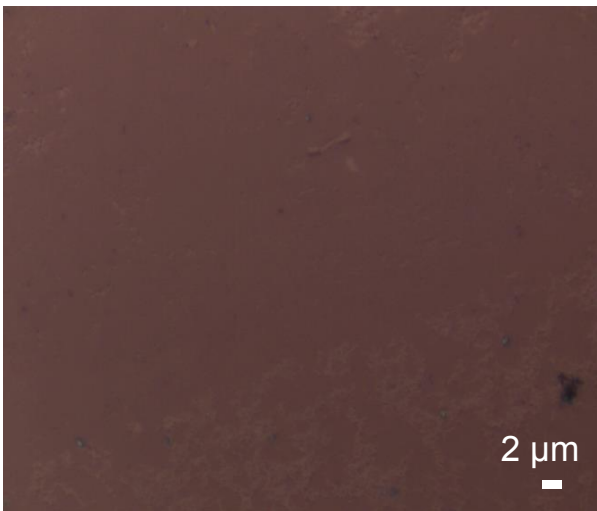
Thin Film Growths



Run	Center		Edge	
	D/G	2D/G	D/G	2D/G
(20,20)	0.16	3.71	0.30	2.54
(60,20)	0.09	4.39	0.39	3.81
(20,40)	0.11	2.57	0.09	4.39
(40,30)	1.46	1.37	1.63	3.81
(60,40)				

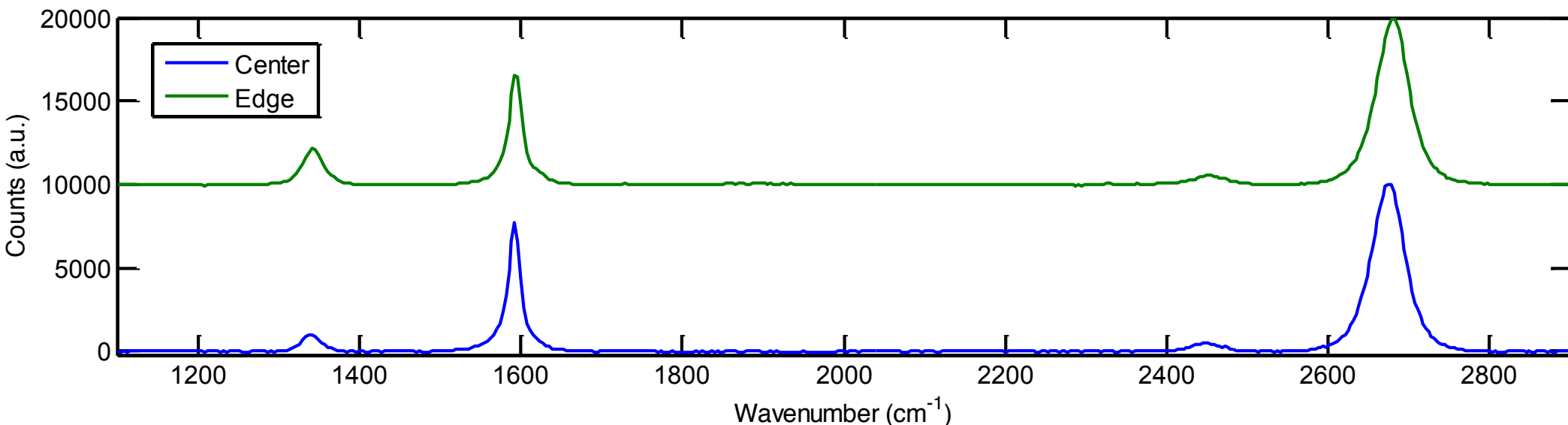
Thin Film Growths

Run 37 (60,40), 50X



Run	Center		Edge	
	D/G	2D/G	D/G	2D/G
(20,20)	0.16	3.71	0.30	2.54
(60,20)	0.09	4.39	0.39	3.81
(20,40)	0.11	2.57	0.09	4.39
(40,30)	1.46	1.37	1.63	3.81
(60,40)	0.19	3.22	0.46	3.32

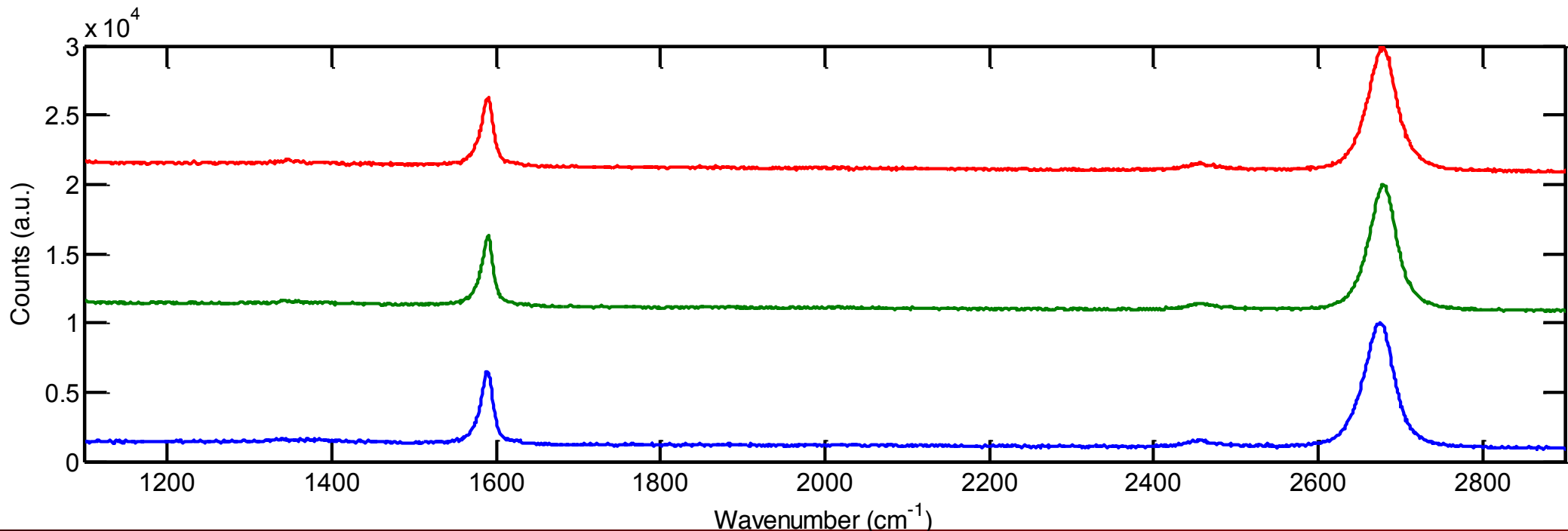
Run 37 (60,40)



High Quality Treatment/Foils

Run 65

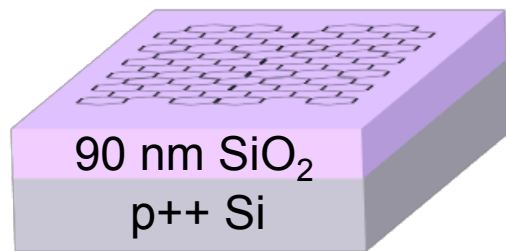
- Experimental growths run by Dr. Yong Cheol
- Substrate – High Purity Cu from JX
- Surface treatment w/ acetic acid
- Reduced Ar flow
- Low pressure



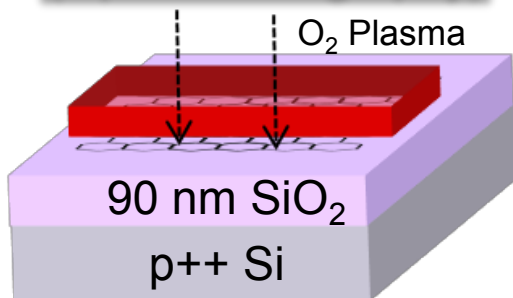
Electrical Characterization

TLM Structures: Fabrication Details

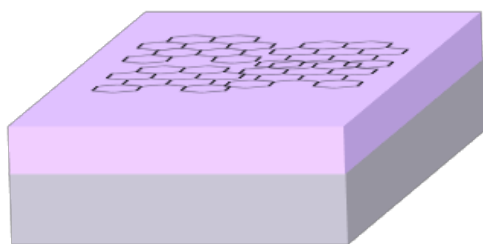
1. Transfer Graphene



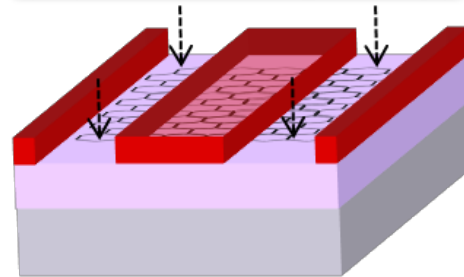
2. Define Channel (Optical Lithography)



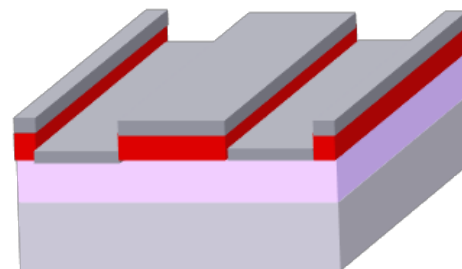
3. Remove PR



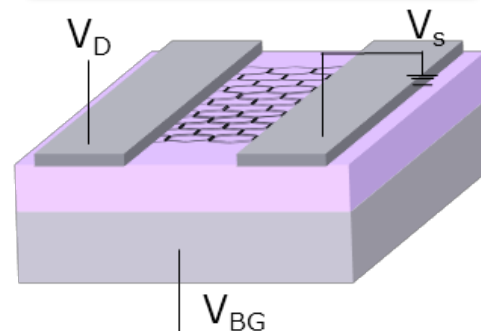
4. Define Contacts (Optical Lithography)



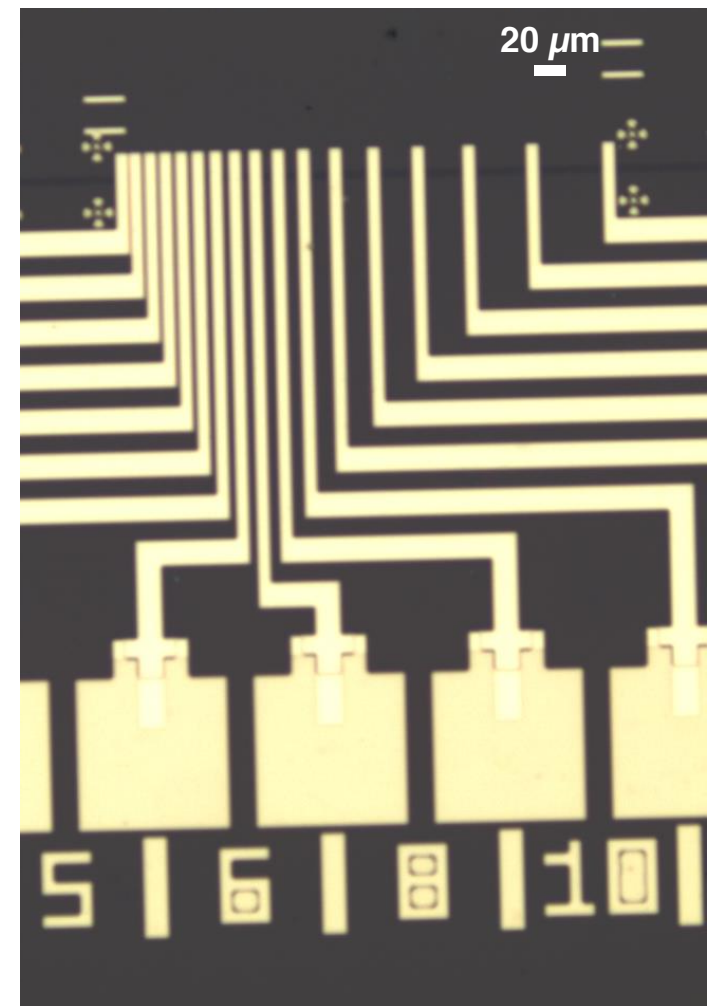
5. Metal Deposition (40 nm Pd, e-beam)



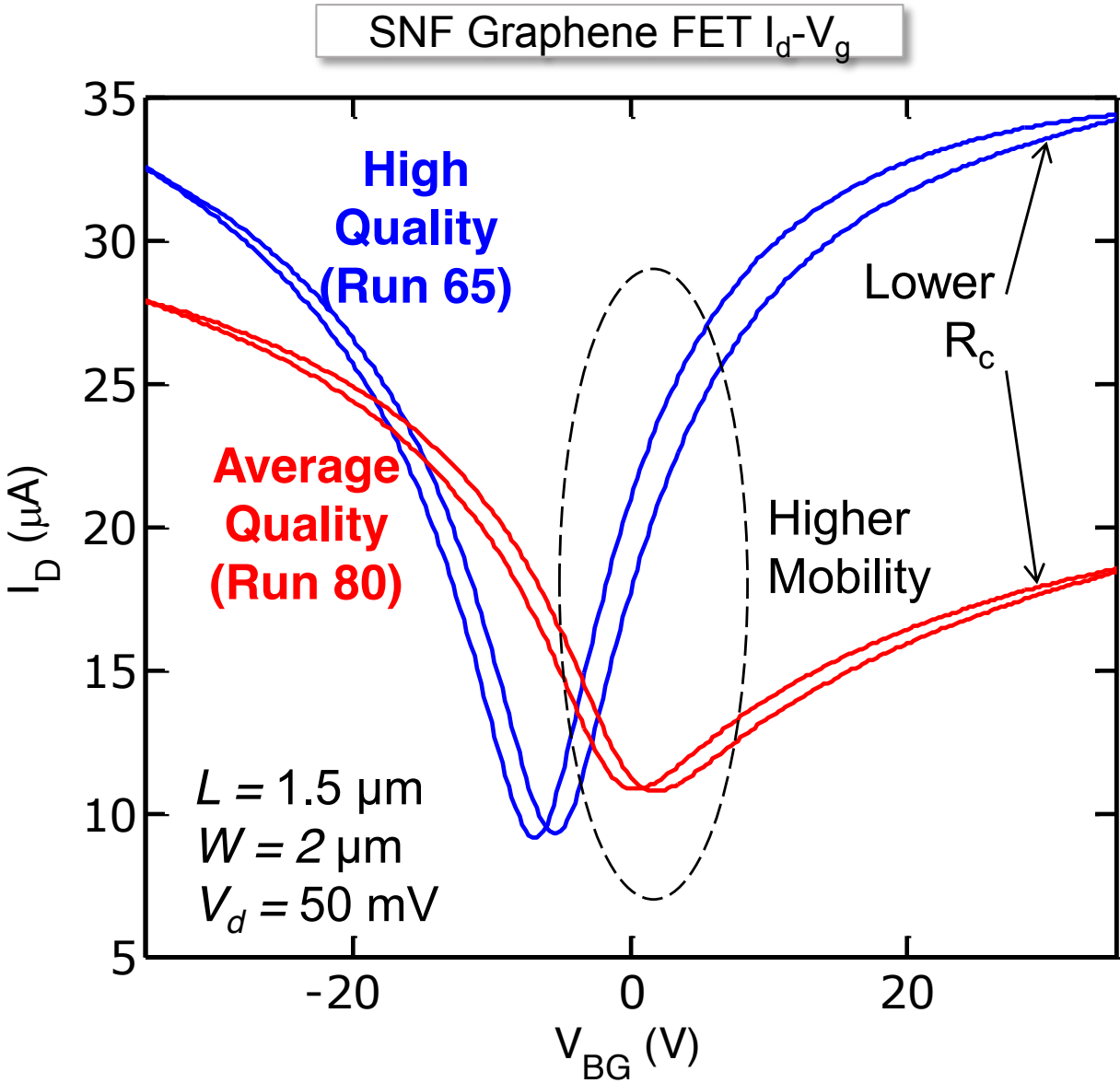
6. Liftoff



TLM Structure
10x Optical Image



Electrical Results – Qualitative Comparison



Fit Model

$$R_{d,fit} = \frac{L}{W} R_s + 2R_c + R_{series}$$

Sheet Resistance

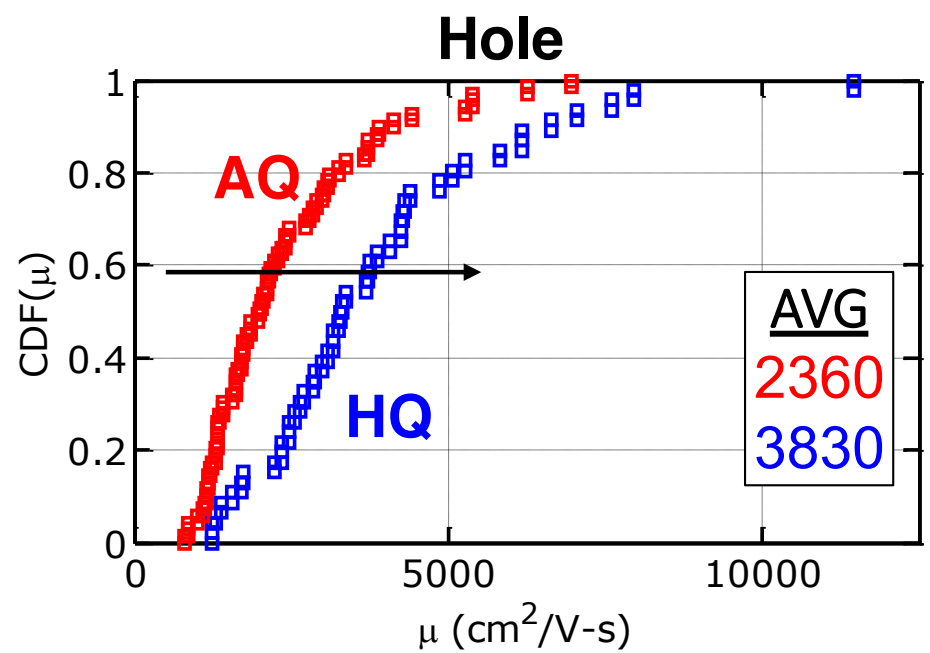
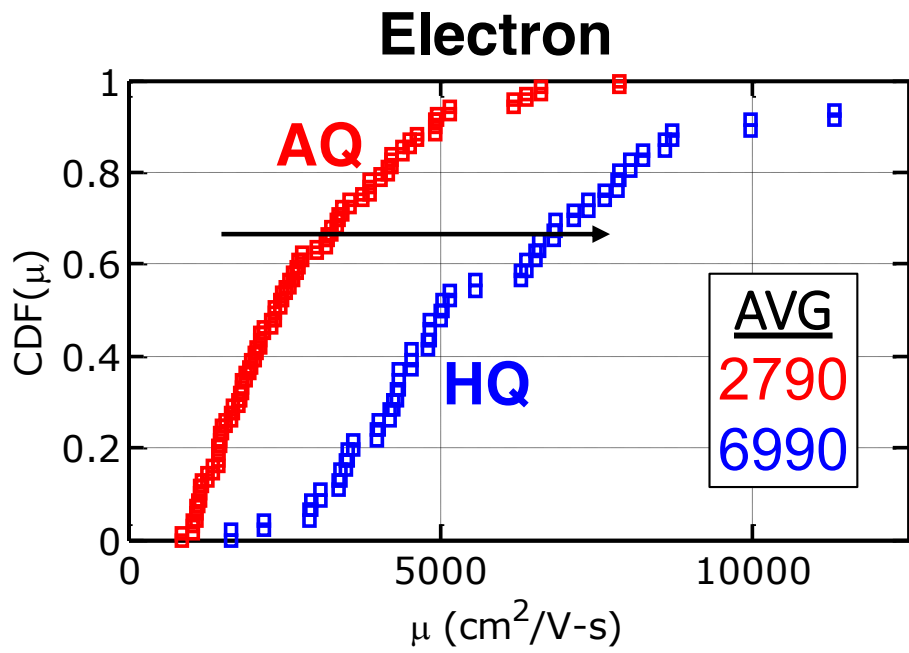
$$R_s = \frac{1}{q\mu_0(n+p)}$$

Contact Resistance

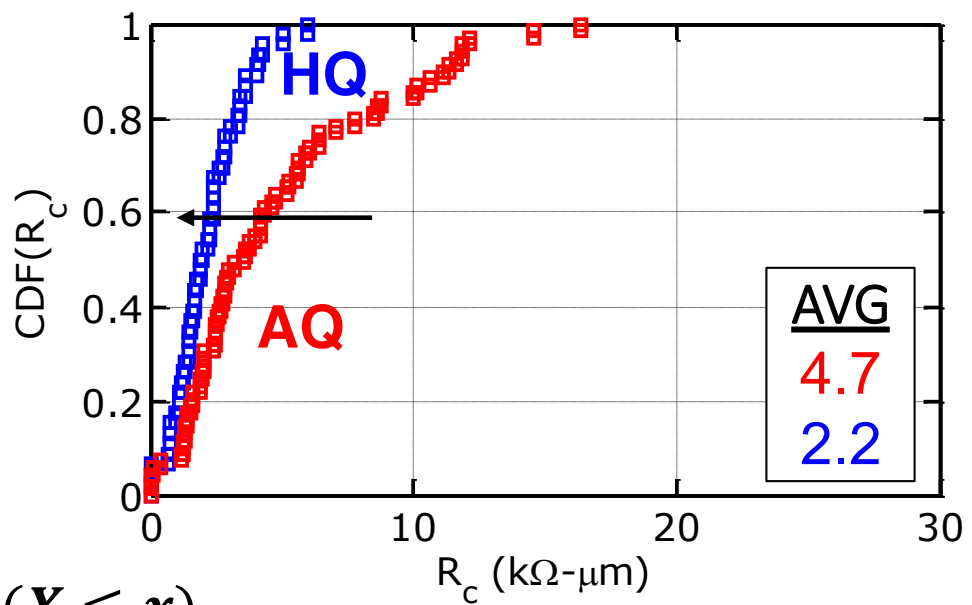
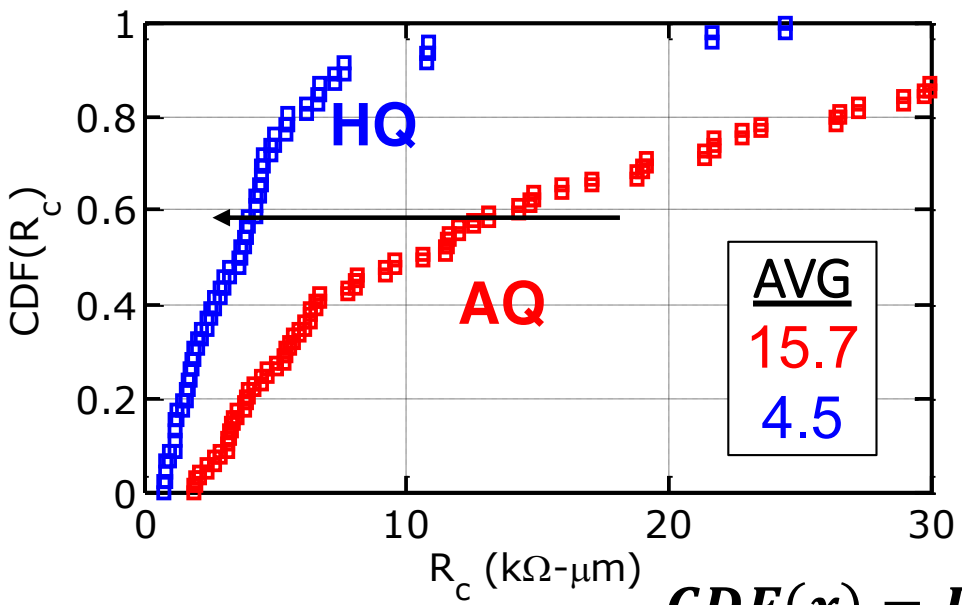
$$R_c = \frac{1}{W} \frac{\rho_c}{L_T} \coth\left(\frac{L_c}{L_T}\right), L_T = \sqrt{\frac{\rho_c}{R_s}}$$

Electrical Results – Statistical Comparison

Intrinsic Mobility



Contact Resistance



$$CDF(x) = P(X \leq x)$$

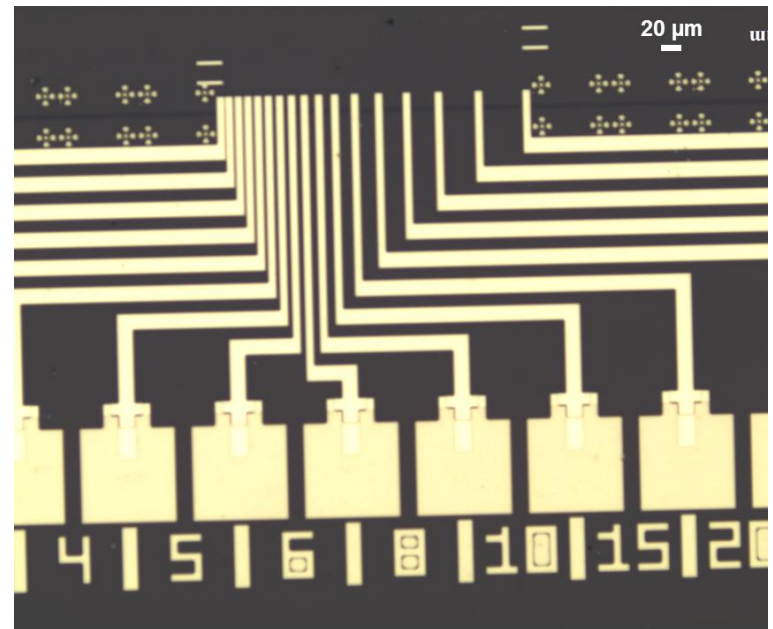
Conclusion

- SNF BM Pro 4" Furnace Development:
 - Recipes refined w/DOE
 - >100 successful growths!
- Mass elec. characterization confirms high graphene quality
- Quality dependent on substrate type, prep:



Type	Cu Purity	Grain Size (μm)	μ_e ($\text{cm}^2/\text{V-s}$)	μ_n ($\text{cm}^2/\text{V-s}$)
AQ	99.8%	0.5-1	~ 2800	~ 2300
HQ	99.9%	>10	~ 6900	~ 3800

SNF now ready for graphene!



Thanks to:

Yong Cheol Shin

Michelle Rincon

Robert Chen

Ted Berg

Aixtron Technical Support: Nigel Bradley, Ken Teo

GFET Fabrication



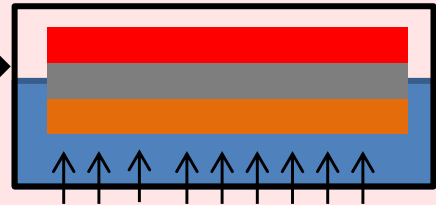
Growth



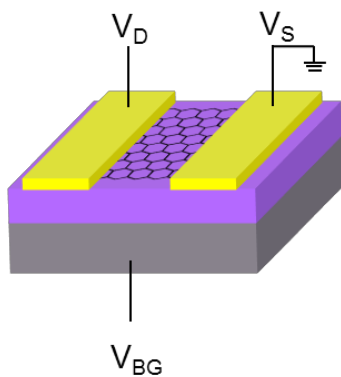
Spincoat
PMMA

Transfer

Etch Copper



Device Fab



PMMA Removal
(Acetone)

Transfer/
Clean



Pop Lab Graphene Model

Total Device Resistance

$$R_{d,fit} = \frac{L}{W} R_s + 2R_C + R_{series}$$

Contact Resistance

$$R_c = \frac{1}{W} \frac{\rho_c}{L_T} \coth\left(\frac{L_c}{L_T}\right) \approx \sqrt{\rho_c R_s} \text{ for } L_c \gg L_T = \sqrt{\frac{\rho_c}{R_s}}$$

Sheet Resistance

$$R_s = \frac{1}{q\mu_0(n+p)}$$

↓

$$n = \frac{1}{2} \left[n_{cv} + \sqrt{n_{cv}^2 + 4n_{ip}^2} \right]$$

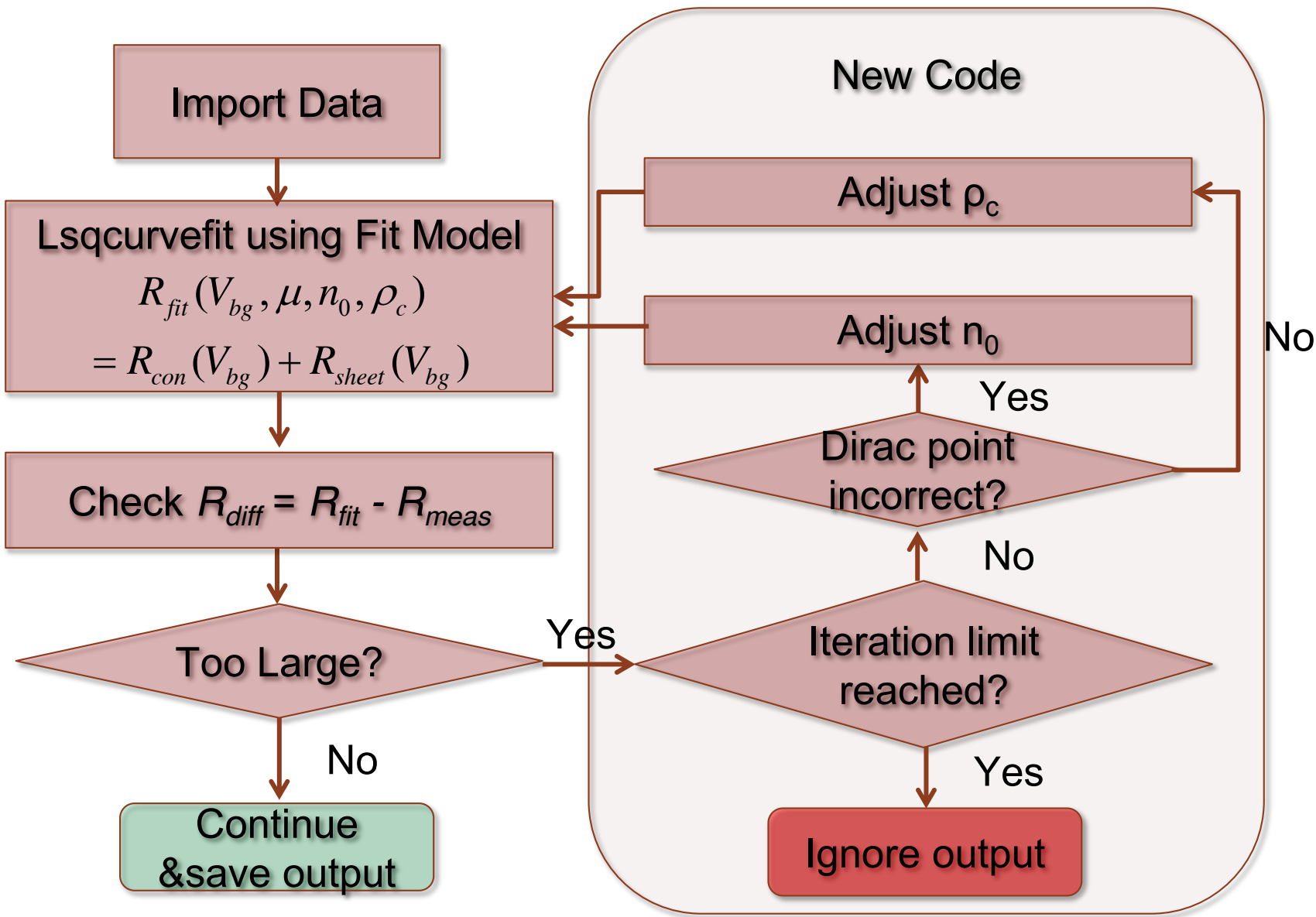
Carrier Con.

Impurity Conc. $n_{ip} = \sqrt{\left(\frac{n_0}{2}\right)^2 + n_i^2}$

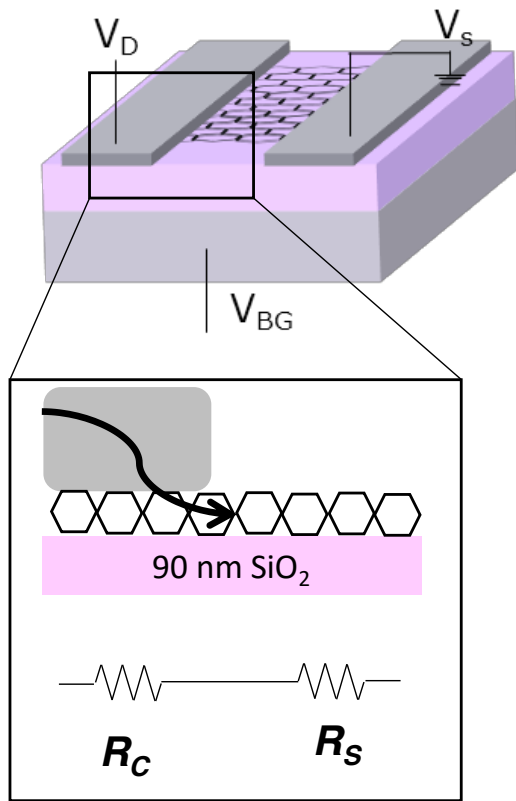
Electrostatic Doping $n_{cv} = C_{ox}(V_0 - V_{bg}) / q$

Thermal Carriers $n_i = \frac{\pi k_b T^2}{6 \hbar v_F}$

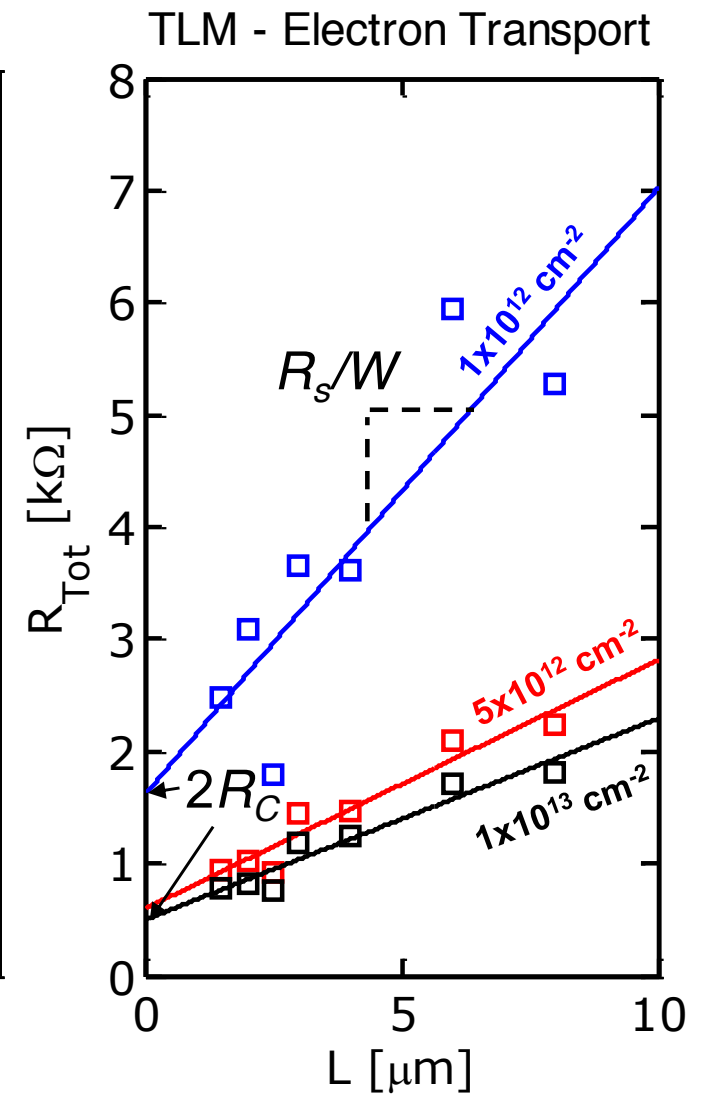
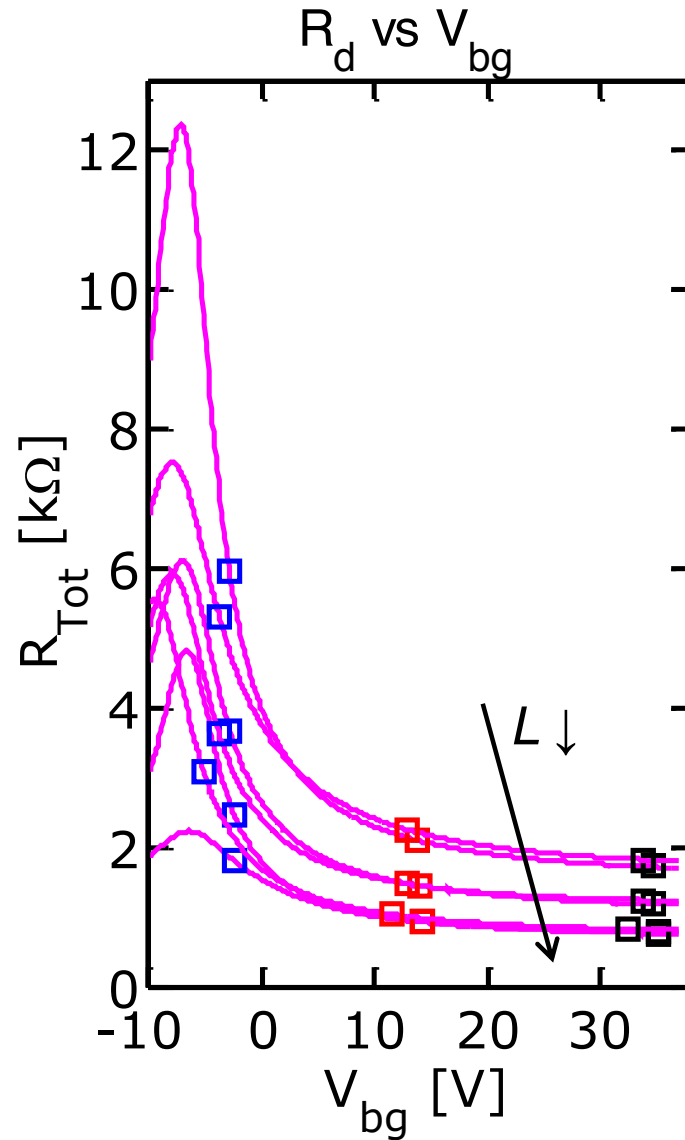
Pop Lab Graphene Model



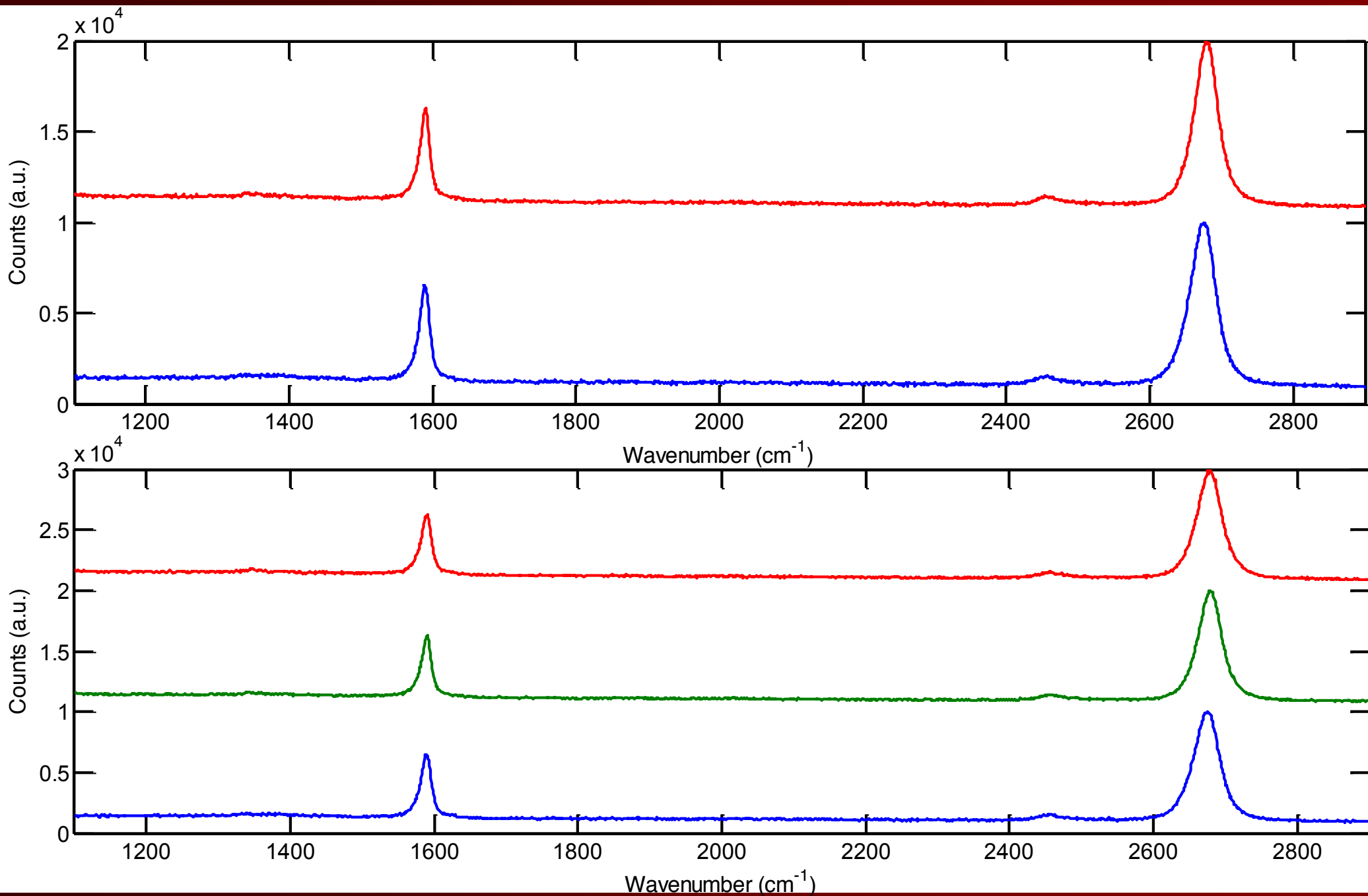
TLM Structures: Extracting R_C

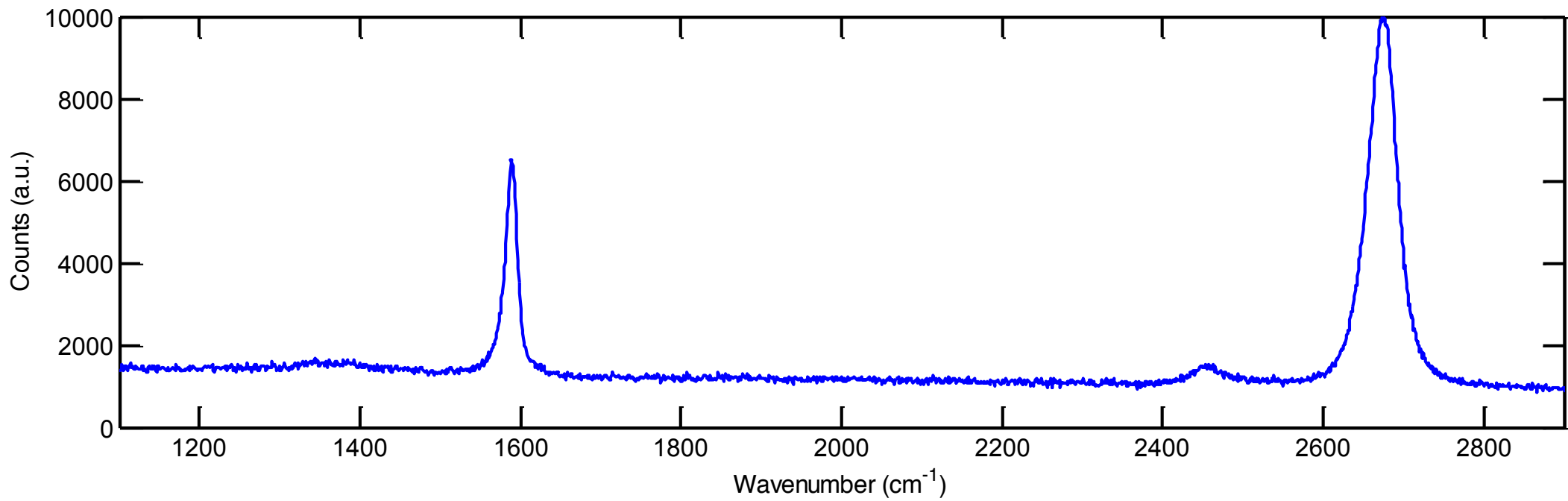


$$R_{TOT} = \frac{R_S}{W} L + 2R_C$$

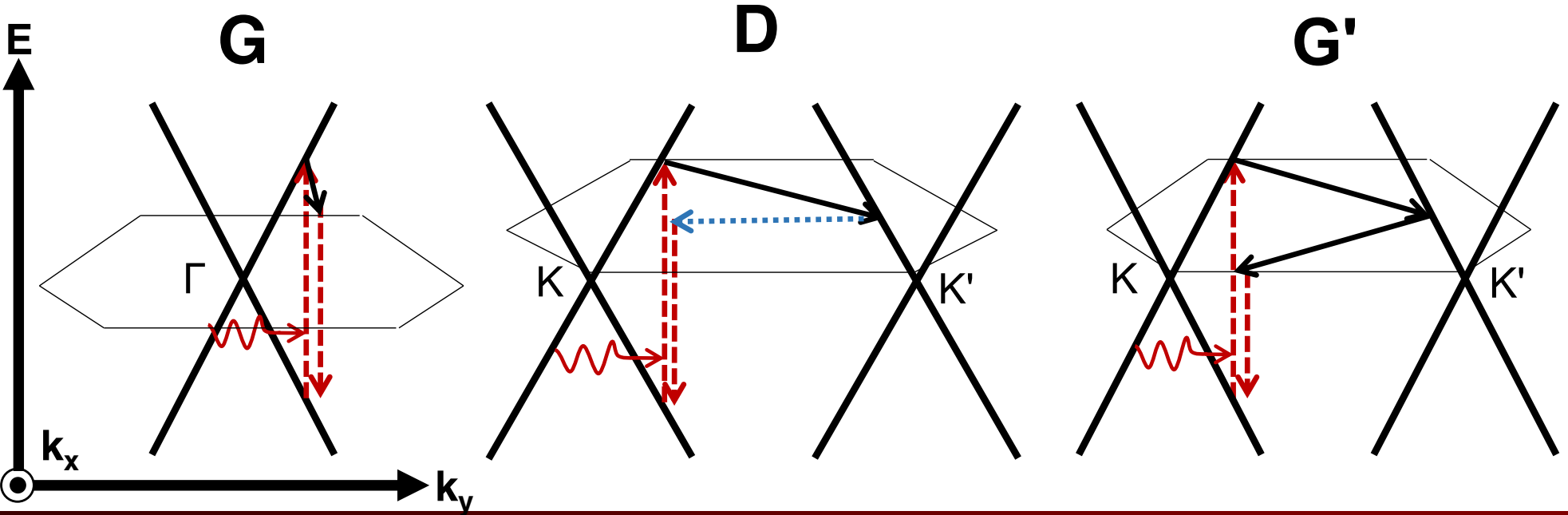
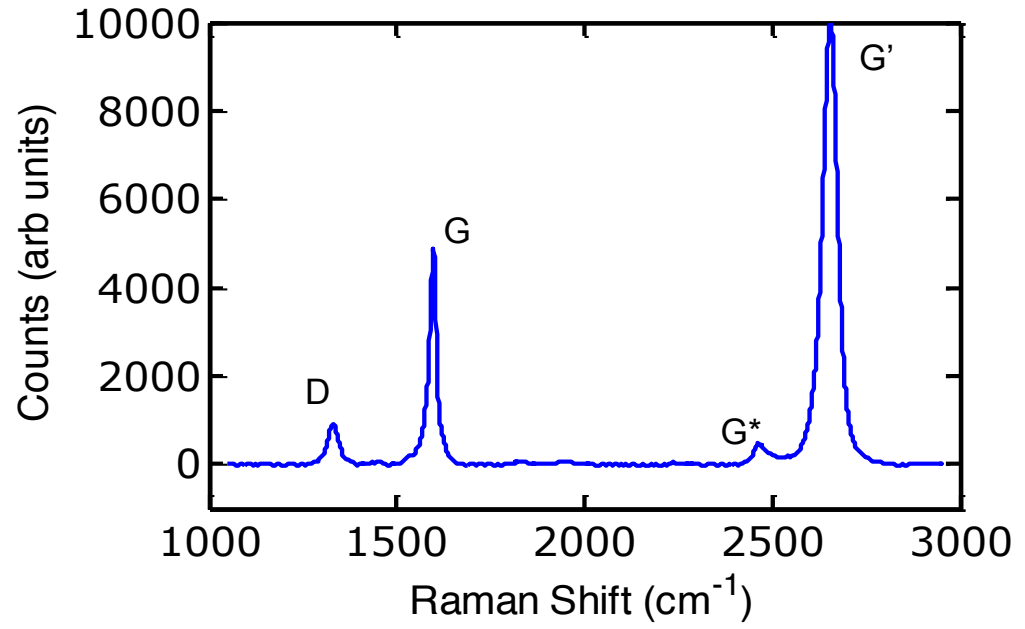
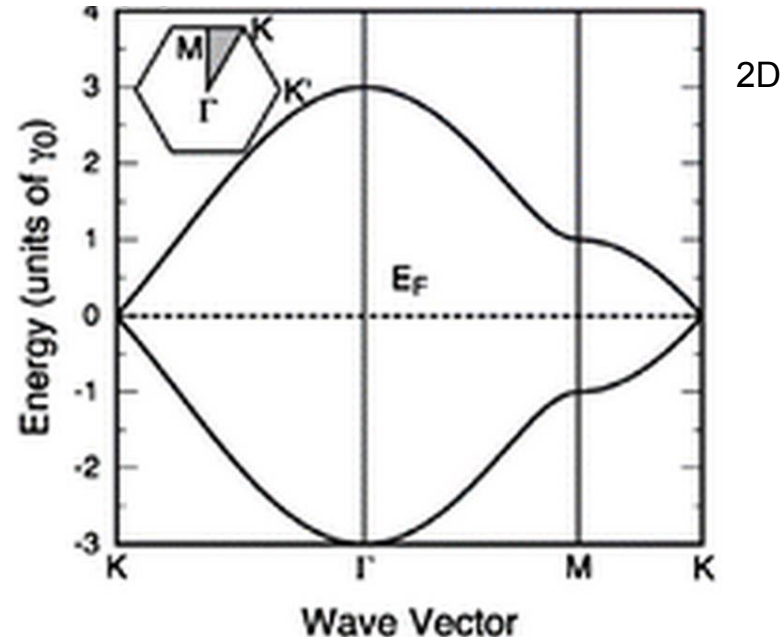


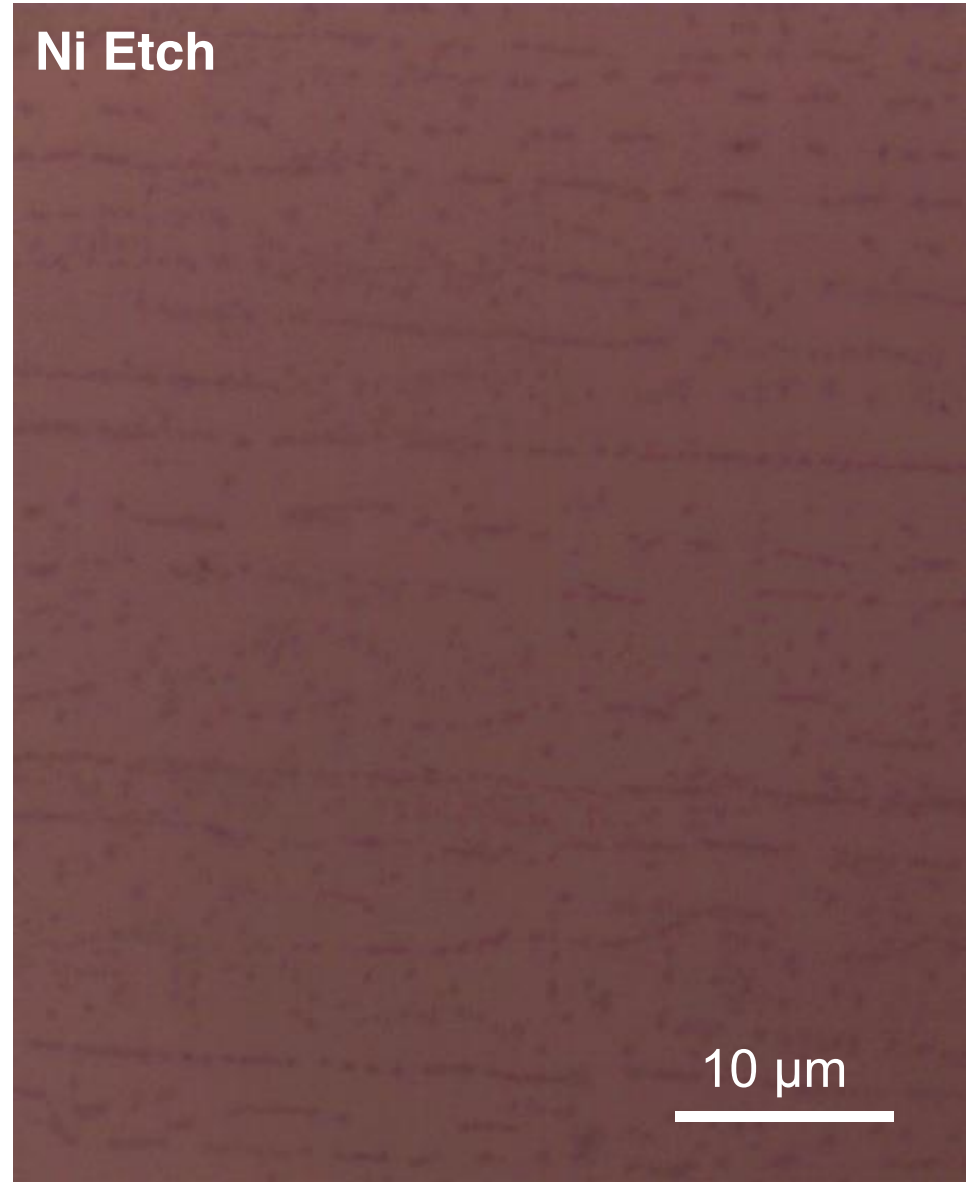
Run 65



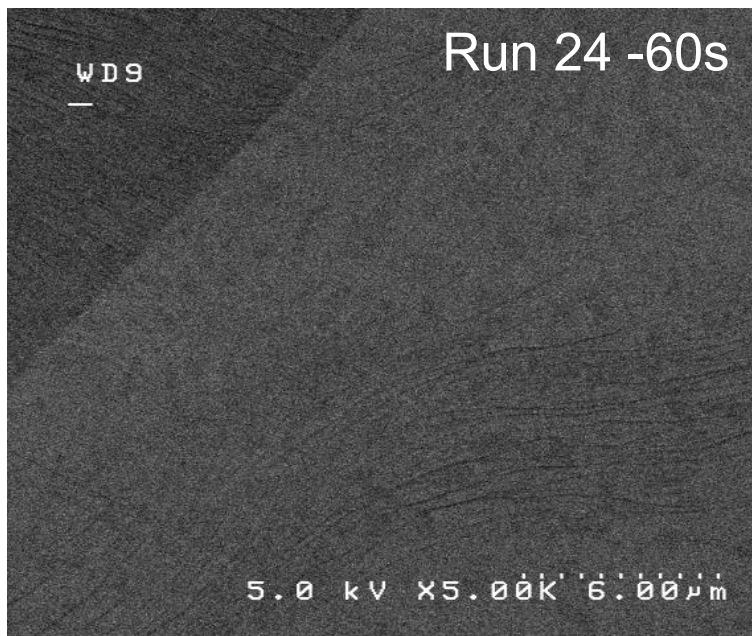
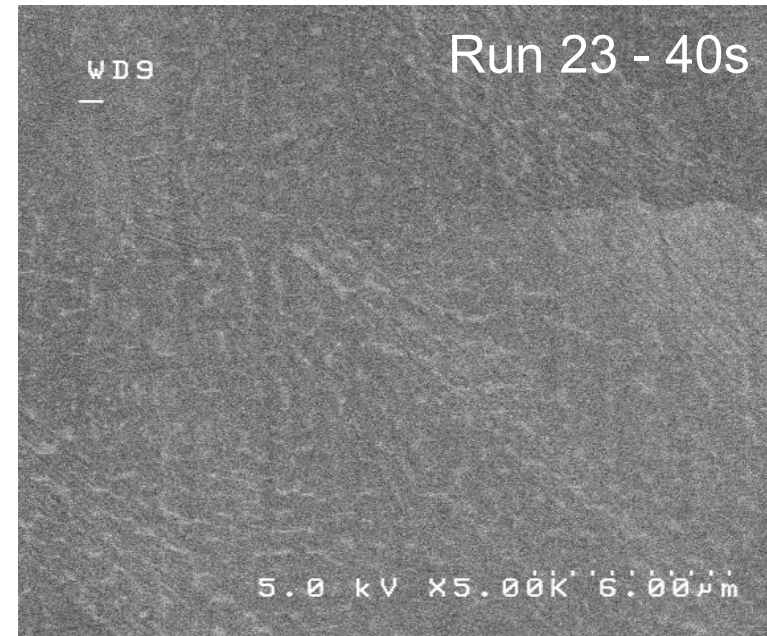
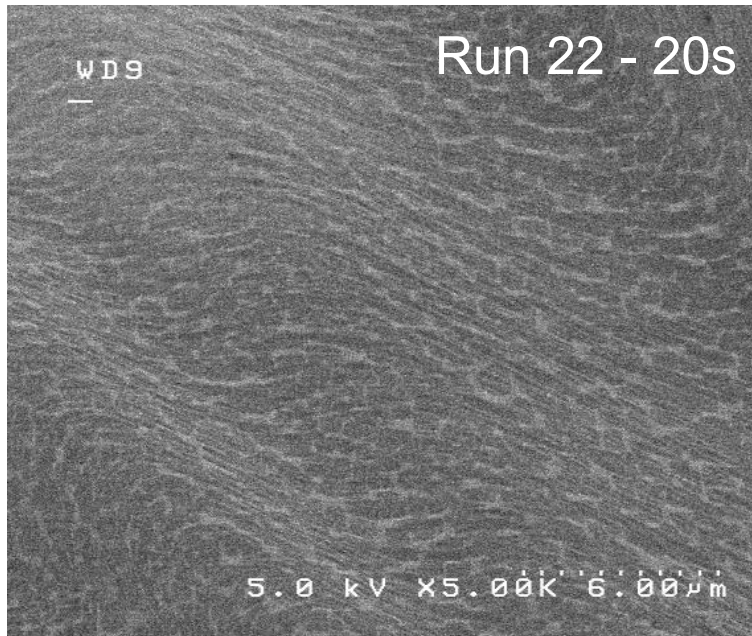


Raman – Qualification Method





Current Growth Progress – Copper Foil Grain Size



- High growth rate
 - Small grain size: $\sim 1 \mu\text{m}$
 - Local bilayer
- $\text{H}_2/\text{Ar}/\text{CH}_4$ flow rate ratio control